

M5Stack Unit 8Angle I2C Protocol																		V1 (FW Version)	
																		2022/11/10	
REG			0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	note
	ANALOG INPUT-12 Bits	0x00 R	VR0- L	VR0- H	VR1- L	VR1- H	VR2- L	VR2- H	VR3- L	VR3- H	VR4- L	VR4- H	VR5- L	VR5- H	VR6- L	VR6- H	VR7- L	VR7- H	value:0~4095
	ANALOG INPUT-8	0x10 R	VR0	VR1	VR2	VR3	VR4	VR5	VR6	VR7									value:0~255
	SWITCH INPUT	0x20 R	SW															value:0~1	
	RGB 24Bits	0x30 W/R	LED0- R	LED 0-G	LED0- B	BR	LED1- R	LED1- G	LED 1-B	BR	LED2- R	LED2- G	LED2- B	BR	LED 3-R	LED3- G	LED3- B	BR	R/G/B:0~255
		0x40 W/R	LED4- R	LED4- G	LED4- B	BR	LED5- R	LED5- G	LED 5-B	BR	LED6- R	LED6- G	LED6- B	BR	LED7- R	LED7- G	LED7- B	BR	BR:0~100
		0x50 W/R	LED8- R	LED8- G	LED8- B	BR													
	Firmware version	0xF0 R														Version		Version: firmware version	
	I2C ADDRESS SETTING	0xF0 W/R																Addr	value: 0~127 default:0x43