

M5Stack Module AIN4-20mA I2C Protocol																	V1 (FW Version)	
																	2023/1/30	
REG MAP (Addr:0x55)		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	note
Raw ADC (16Bits)	0x00 R	ADC1-L	ADC1-H	ADC2-L	ADC2-H	ADC3-L	ADC3-H	ADC4-L	ADC4-H									ADC: ADC = (ADC-L + ADC-H * 256)
Current	0x20 R	Current 1-L	Current 1-H	Current 2-L	Current 2-H	Current 3-L	Current 3-H	Current 4-L	Current 4-H									Current: Current = (Current-L + Current-H * 256)
Firmware Version	0xF0 R															Version		Version: firmware version number
I2C Address	0xF0 R																Address	Address: I2C Address