

M5Stack Hat Encoder I2C Protocol																	V1 (FW Version)		
																	2022/11/1		
REG MAP (Addr:0x42)		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	note	
Counter Value (accumulate)	0x00 R/W	Cnt- byte0	Cnt- byte1	Cnt- byte2	Cnt- byte3													Cnt: -2147483648-2147483647 (will be reset after set reg 0x40)	
Increment Value	0x10 R	Inc- byte0	Inc- byte1	Inc- byte2	Inc- byte3													Cnt: -2147483648-2147483647 (will be reset after get)	
Button Value	0x20 R	BNT																BNT: 0~1	
RGB	0x30 R/W	LED-R	LED-G	LED-B														R/G/B: 0~255	
Counter Reset	0x40 W	Cnt- RST																RST: write 1 to reset counter	
Firmware Version	0xF0 R															Version		Version: firmware version number	
I2C Address	0xF0 R/W																Address	Address: 1~127	